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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Kazuhiko TAKADA

Serial Number: 09/528,296

Group Art Unit: 2811

Examiner: Ori NADAV, Ph.D.

March 17, 2000

SEMICONDUCTOR DEVICE HAVING A GUARD RING

AMENDMENT UNDER 37 CFR §1.111

Commissioner for Patents Washington, D.C. 20231

September 10, 2001

Sir:

In response to the Office Action dated May 9, 2001 please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claim 1 so as to read as follows:

S10

1. (Amended) A semiconductor device, comprising:

a substrate; and

a multilayer interconnection structure formed on said substrate,

said multilayer interconnection structure including: at least first and second interlayer insulation films provided on said substrate; and a guard ring pattern embedded in each of said first